

RESUME

Name: TALABATTULA.VISWANADHAM

Department: E.C.E

Designation: Assistant. Professor

Date of Birth: 05-08-1980

Father Name: MALLESWARARAO

Mother Name: KRISHNA KUMARI

Reservation Category if Any: BC-B

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SRIKAKULAM (DT);

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Date of Joining in the Institution: 12-07-2011

Academic Excellence:

QUALIFICATION	BOARD / UNIVERSITY	DIVISION	YEAR
Ph.D	AU, Visakhapatnam	Pursuing	Registered in 2015-16
M. Tech (VLSI System Design)	JNTUK, kakinada	FIRST class	2011
B. Tech	JNTUK	FIRST class	2003

Total Years of Experience (Teaching/Industry): 11.5 Years



Period		Organization/Institution	Designation
June 2003	July 2005	Phenomena software sol,Hyd	Software eng.
Aug 2005	Oct 2007	PCET,Hyd	Assistant professor
Nov 2007	Oct 2008	PITAM,Palasa,SKLM	Assistant professor
Oct 2010	July 2011	PITAM,Palasa,SKLM	Assistant professor
July 2011	Till date	AITAM,Tekkali	Assistant professor

Areas of Interest:

1. Micro Processors
2. Micro Controllers
3. VLSI
4. Digital signal processors
5. Digital Signal processing

Guidance of Academic Projects: 07

- i) Doctoral Projects : -
- ii) Masters Projects : 01
- iii) Bachelor Projects : 06

Sponsored Research Projects: -

S. No.	Sponsoring Agency	Title of the Project	Grant
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Funding from external agencies:

S. No.	Sponsoring Agency	Title of the Program	Grant
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Honors and awards received: -

Memberships of Professional Societies and Other Organizations:

1. MSEMCE(INDIA)

Other Responsibilities:

1. Class teacher
2. Student counselor
3. Microsoft faculty connection

Details of Book Publications: -**Details of Publications: -****International Journals:**

1. R.Thaviti raju, K.Chidambararao, T.Viswanadham, B.Chinnarao “Design and Realization of Practical FIR Filters based on CSD and DA algorithms”, IOSR Journal of VLSI and Signal Processing (IOSR-JVSP), Volume6, Issue4,Ver.II(Jul.-Aug. 2016) , pp.40-49.
2. K.Chidambararao, E.Jaya, T.Viswanadham, L.Rambabu “Design and Realization of Practical FIR Filter using hybrid window and CSD algorithm” IOSR Journal of VLSI and Signal Processing (IOSR-JVSP), Volume6, Issue6, Ver.II(Nov.-Dec.2016), pp.21-28

International Conferences:-

S.No	Date	Name of the Program	Name of the Paper	Host Institute
1.	Dec 2016	International conference on Electrical,Electronics & Optimization Techniques(ICEEOT)	An Efficient Signal Processing Algorithm for Accurate Detection of Characteristic Points in Abnormal ECG Signals	Chennai

National Conference:

S.No	Date	Name of the Program	Name of the Paper	Host Institute
4	24 th &25 th Feb,2017	Two day national conference on Recent Advances in Biomedical signal, Image and video processing (RABSIVP-2K17)	Denosing of EEG signal using Frft basedbarlett window	AITAM, Tekkali

Details of Conferences / Workshops / Refresher Courses Organized:-

1. Two day national conference on Recent Advances in Biomedical signal, Image and video processing (RABSIVP-2K17) on 24-25.02.2017.

Details of Workshops / Refresher Courses Attended:

Internal:

S. No.	Dates	Name of The Programme	Host Institution
1	19 th January, 2013.	A One day workshop on Outcome Based Education (In compliance with NBA Accreditation) on	AITAM, Tekkali
2	24 th -28 th April,2012	Two week workshop by Wipro Mission 10X (FDP)	AITAM, Tekkali
3	(27,28-DEC-2012)	Two day workshop on Cyborg-the Robotics by KYRION ROBOTICS CLUB at AITAM	AITAM, Tekkali
4	SACAM-12	A Two day National workshop on “Soft-Computing Applications to Communication And Management”(SACAM-12)	AITAM, Tekkali
6	24 th -28 th April,2012	WIPRO Mission -10X	AITAM, Tekkali
9	1 st -2 nd Mar, 2013	Advances in Signal and Image Processing (ASIP-2013)	AITAM, Tekkali
10	12 th June-2 nd August 2014	Pedagogy for Effective use of ICT in Engineering Education	AITAM, Tekkali
11	15 th -16 th Nov ,2014	Twenty First Century the dawn of Digital Era:Pedagogical Issues of Technical Teachers	AITAM, Tekkali
12	6 th Dec,2014	A One day workshop on Outcome Based Education	AITAM, Tekkali
13	20 th -21 th Feb,2015	Emerging trends in Signal Processing and Communication Engineering	AITAM, Tekkali
14	05.06.2016	Intellectual property rights(IPR) in E& IT sector	AITAM, Tekkali
15	22-27.09-16	Optimization techniques for electrical engineering applications	AITAM, Tekkali
16	26 th Sep-1 st Oct 2016	Advances in Communication Technology	AITAM, Tekkali

External: -

S. No.	Dates	Name of The Programme	Host Institution
1	30-31.03-2017	National workshop on Graphical System Design using LabView	AU, Visakhapatnam
2	22-27.08.2016	Medical signal and Image processing (MSIP 2016)	NIT Raipur
3	26-27,04-2013	National workshop on Advanced VLSI methodology	JNTUK
4	14-25.05.2012	System Design using FPGA	CDAC, Hyderabad

Other Achievements:

1. Ratified as Asst. Professor in 2014 in JNTUK selections.

Signature