

15-16 STD - II-A - I-SEM  
 B.B. Blackboard  
**LESSON PLAN**

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Correct Upon
(1)	5/8/15	Introduction	1	B.B		
(2)	6/8/15	Number base conversions examples problem solving	1	B.B		
(3)	10/8/15	Binary arithmetic problem solving				
(4)	10/8/15	problem solving	1	B.B		
(5)	12/8/15	Complement 8's and 10's complement problem solving.	1	B.B		
(6)	13/8/15	problem solving	1	B.B		
(7)	17/8/15	burst operation using complement				
(8)	13/8/15	problem solving	1	B.B		
(9)	19/8/15	codes, weighted, Non-weighted. 2421, 5421, codes examples				
(10)	20/8/15	gray, ASCII codes, examples.	1	B.B		
(11)	24/8/15	B.C.D addition excess 3 addit problem solving				

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
(12)	24/8/15	Parity Hamming codes examples	1	BB		
(13)	26/8/15	Logic gates truth tables	2	BB		
(14)	27/8/15	Universal gates truth tables				
(15)	31/8/15	Logic gate construction examples problem solving	2	BB		
(16)	31/8/15	Boolean theorem ex <sup>pl</sup>	2	BB		
(17)	2/9/15	Boolean creations, concept of SOP, POS	2	BB		
(18)	3/9/15	Standard SOP, POS, examples for minimization, examples	2	BB		
(19)	7/9/15	Multi-level, NAND, NOR Realization Examples	2	BB		
(20)	7/9/15	Problem Solving	2	BB		
(21)	14/9/15	K-map - Rule Minimization 3V-map Example practice problem solving	2	BB		
(22)	14/9/15	4-V map, SOP, POS minimization problem solving	2	BB		
(23)	16/9/15	5-V-map minimization problem solving	2			

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Completed Upon
(24)	12/9/15	Code converters K-map problem solving	2	BB		
(25)	21/9/15	Introduction to combinational Circuit Examples Tabular-Map	3	BB		
(26)	21/9/15	Design of HA, FA, Hs, FHS with examples	3	BB		
(27)	23/9/15	Applications of P.A.S 4-bit Parallel-adder	3	BB		
(28)	24/9/15	4-bit add/subtract BCD adder	3	BB		
(29)	28/9/15	Carry-A-look ahead adder, design of decoder	3	BB		
(30)	28/9/15	Problems on decoder applications of decoder	3	BB		
(31)	30/9/15	Encoder, MUX, applications	3	B		
(32)	1/10/15	Demux, applications, priority Encoder	3	BB		
(33)	5/10/15	Comparator, 7-seven-seg- display	3	BB		
(34)	5/10/15	Introduction to Sequential	4	BB		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
(35)	7/10/15	SR - No & analysis, NAND, analysis, truth table	4	BB		
(36)	8/10/15	Characteristic equation, derivation, JK - analysis, truth table	4	BB		
(37)	12/10/15	JK - Characteristic equation, D, T analysis, T, T, analysis	4	BB		
(38)	12/10/15	Excitation table of SR, JK, analysis	4	BB		
(39)	12/10/15	Excitation table of D, T gate, problem solving	4	BB		
(40)	19/10/15	conversion of ffs, SR $\rightarrow$ JK, JK $\rightarrow$ D	4	BB		
(41)	19/10/15	T $\rightarrow$ D, D $\rightarrow$ JK, all possible conversions	4	BB		
(42)	21/10/15	Design of ripple counter, MOD - 10 counter, examples	4	BB		
(43)	22/10/15	problems on MOD - K counters, gate problem discussion	4	BB		
(44)	26/10/15	Design of asynchronous counters, up/down counters	4	BB		

Design counters,  
Design of  
Risc Johnson  
counter.

28/10/15 Design of  
shift registers 4 BB

Modes  
Explanation

29/10/15 Bi-directional  
shift reg,  
universal  
shift register 4 BB

31/11/15 Design of  
counter, buffer  
registers 4 BB

12/11/15 Gate problem  
discussion,  
problem solving 4 BB

14/11/15 Introduction to  
VHDL programming 5 BB

15/11/15 Base language  
elements  
programming  
models. 5 BB

programming of  
logic gates

19/11/15 Behavioural  
Modelling,  
MUX, decoder,  
sop, sub.  
programming 5 BB